

Criteria 2- Teaching- Learning and Evaluation

Key Indicator- 2.3. Teaching- Learning Process

ICT supportive learning

E-learning resources:

Google classroom, Virtual Lab, Online tests, Use of NDLI portal, AICTE PARAKH

Dr. Deven Shah

Principal Principal

Shree L. R Tiwari College of Engineering Kanakiya Park, Mira Road (E.)





SHREE L. R. TIWARI COLLEGE OF ENGINEERING

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(Approved by AICTE, Govt. of Maharashtra & Affiliated to University of Mumbai)

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DEPARTMENT OF INFORMATION TECHNOLOGY

AY 2021-22

Summary of Faculty using ICT tools

Sr.	Faculty Name	Name Of Activity	Method/	Academic	Subject		
No.			Approach used	year			
1	Mrs. Aarti Puthran	Quiz	Google form	21-22	AT		
2	Mrs. Madhuri Gedam	Quiz	Kahoot	21-22	DevOps		
3	Mr.Saurabh Suman	Assignment	Google form	2021-22	UNIX		
4	Mrs. Komal Champanerkar	Quiz	Google form	2021-22	DSA		



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1/10/2023.



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Automata Theory Quiz 1

- * Required
- 1. Email *
- 2. Roll No *
- 3. Name of Student *

Solve All Questions

4. There are _____ tuples in finite state machine. *

1 point

- Mark only one oval.
- () 6
- (unlimited



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A TM is expressed as a 7-tuple (Q, T, B, Σ , δ , q0, F) where: δ is *

1 point

Mark only one oval.

- $\bigcirc (b) Q \times (\Sigma \cup \{\epsilon\}) \times S \times Q \times S *$
- \bigcirc (c) $Q \times T \rightarrow Q \times T \times \{R\}$
- \bigcirc (d) Q \rightarrow T \times {L,R}
- Finite State Machine can recognize *

1 point

Mark only one oval.

- (a)Context Free Language
- (b) Regular Language
- (c) Context Sensitive Language
- (d) Recursive Enumerable Language
- For a given Moore Machine, Given Input='101010', thus the output would * 1 point be of length:

Mark only one oval.

- (a) |Input|+1
- (b) [Input]
- (c) |Input-1|
- (d)2|Input|



Phree L. R. Tiwari Gol Kanakiyo/2023,

Theory (Quiz 1	https://docs.google.com/forms/u/0/d/1Dc	87pvHPvvl96mRF
8.	Which machine is much ma	model of a General Purpose	* 1 point
	Mark only one oval.		
	(a) Pushdown Automata (b) Finite Automata (c) Non Deterministic automata		
	(d) Turing machine		
9.	Turing Machine can update symbols on update symbols on tape.	its tape, whereas the FA cannot	* 1 point
	Mark only one oval.		
	True		
	() False		,
10	Which of the following problems:		
10.	and the solution is greatering to solv	able? *	1 point
	Mark only one oval.		
	(a) Writing a universal Turing machin		
	(b) Determining of an arbitrary Turing	g machine is an universal Turing machi	ine
	(c) Determining of a universal Turing instructions for some k	machine can be written for fewer than	k
	(d) Determining of a universal Turing	machine and some input will halt	



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11. For given production rules, A \rightarrow aA | a | b How many steps are required * 1 point to form aab?

Mark only one oval.

- ()2
- () 3
- ()4
- () 5
- 12. Which machine is used to accept Context Free Language? *

1 point

- Mark only one oval.
- (a) Deterministic Finite Automata (DFA)
- (b) Non Deterministic Finite Automata (NFA)
- (c) Finite State Machine (FSM)
- (d) Push Down Automata (PDA)

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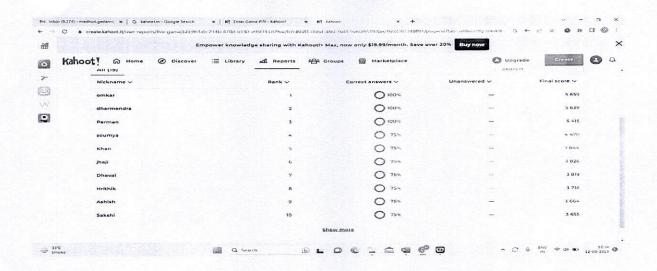
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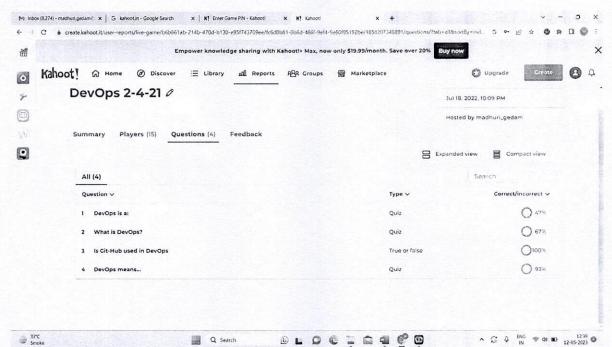
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Beautifully played, madhuri_gedam! View and share a detailed report

visual report that will support formative assessment and help you improve We hope you had fun playing. "DevOps 2-4-21"! Check out your detailed, student learning outcomes:



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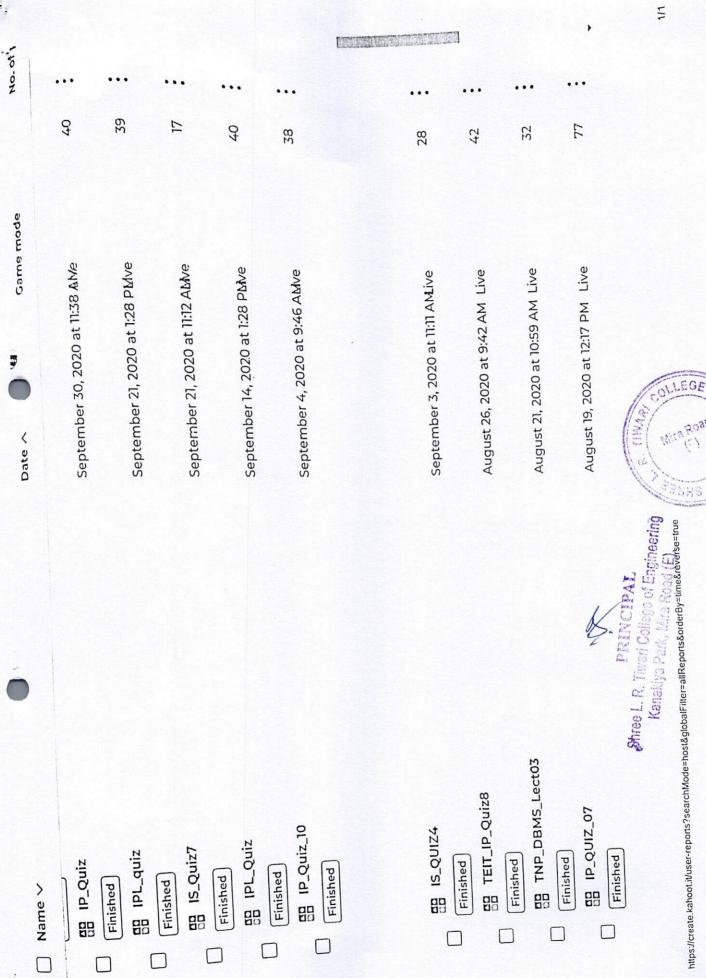
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ON ENGINEER.

Unix Assignment 1

* Indicates required question

- Email *
- 2. Name *
- 3. Roll No *
- Branch * 4.

Mark only one oval.

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- Sem-IV *



6.	Which of the following correctly describes Array data types in Pen?
	Mark only one oval.
	These are simple variables. They are preceded by a dollar sign (\$).
	These are ordered lists of scalars that you access with a numeric index which starts with 0.
	These are unordered sets of key/value pairs that you access using the keys as subscripts.
	None of the above.
7.	Which of the following method shifts the first value of the array off and returns 1 point it, shortening the array by 1 and moving everything down?
	Mark only one oval.
	wark only one ovar.
	push @ARRAY, LIST
	pop @ARRAY
	shift @ARRAY
	unshift @ARRAY, LIST
8.	Which of the following statement terminates the loop statement and transfers 1 point execution to the start of the loop?
	Mark only one oval.
	next
	☐ last
	continue
	redo



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	9.	Which of the following function returns current position of a pointer in a file?	1 point
		Mark only one oval.	
		rename	
		tell	
		seek	
		None of the above.	
	10.	Which of the following is correct about lexical variables?	1 point
		Mark only one oval.	
		Lexical variables are private variables created using my operator.	
		Lexical variables are global variables.	
		Both of the above.	
		None of the above.	
	11.	Which one of the following is the correct syntax for grep command?	1 point
		Mark only one oval.	
		grep options filename(s)	
		grep options pattern	
)		grep pattern filename	
		grap antions nattern filename(s)	



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	12.	When the pattern is not found in a file, grep command silently returns prompt.	the 1 point
		Mark only one oval.	
		True	
		False	
	13.	Which of the following commands is known as stream editor?	1 point
		Mark only one oval.	
		sed	
		grep	
		grep tr	
		u	
	14.	To suppress the behavior of 'p' command of sed, we use optio	1. 1 point
		Mark only one oval.	
)			
	15.	Describe egrep command. *	1 point
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Unix Assignment 1

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Name

57 responses

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Aditya Prakash Waskar

Dhaval Joshi

Yadav Ramashish Mohan

SATYAM SINGH

KHAN ANAS ANJUM

Aishwarya Shrivastava

Yadav Ramesh Bhorik

Satyendra Jaiswar

SWARANGI SAWANT

Prajapati Pradeep DineshKumar

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Satya Prakash Mishra

Dharmendra Phoolchand Mallah

Singh Vishal Anil

SINGH RISHABH PRADEEP

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dipanshu

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Aditya Vinodkumar Mishra

Aman Manish Rastogi

Yadav Nishant Rajendra

Rohankumar Dineshkumar Pal

Rohit balasaheb Ghongade

Utkarsh Mishra

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Harsheet Sharma

Aditya Gupta

Hrithik Rajendra Dwivedi

Jaiswar Satyendra Mohan

Sharma Shrikrishnagopal Ramvilas

Varma Aniket

Saloni vinay pandey





Roll No

57 responses



College of Engineering Kanakiya and (E).



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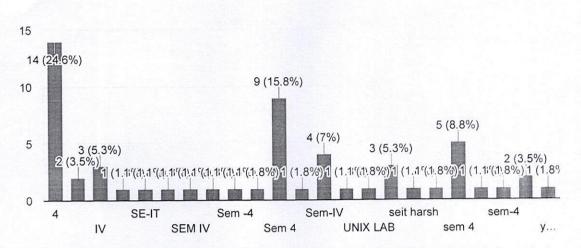




Sem-IV



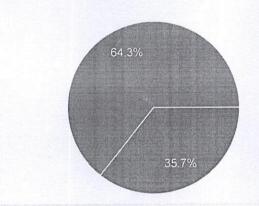
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Which of the following correctly describes Array data types in Perl?

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56 responses

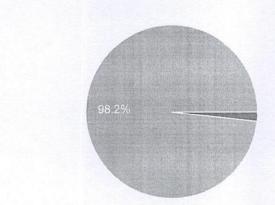


- These are simple variables. They are preceded by a dollar sign (\$).
- These are ordered lists of scalars that you access with a numeric index which starts wit...
- These are unordered sets of key/value pairs that you access using the keys as subscripts.
- None of the above.

Which of the following method shifts the first value of the array off and returns it, shortening the array by 1 and moving everything down?

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- push @ARRAY, LIST
- pop @ARRAY
- shift @ARRAY
- unshift @ARRAY, LIST



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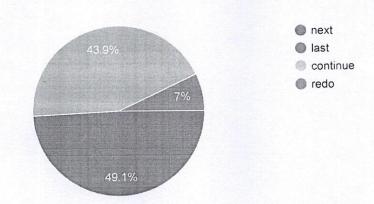
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Which of the following statement terminates the loop statement and transfers execution to the start of the loop?

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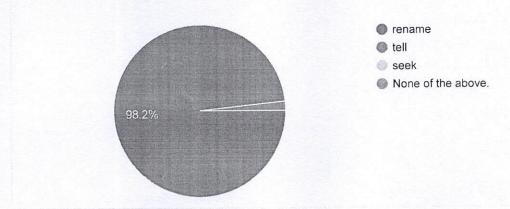
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Which of the following function returns current position of a pointer in a file?

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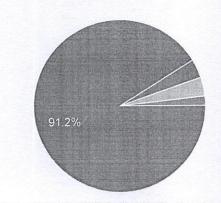
57 responses



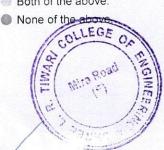
Which of the following is correct about lexical variables?

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- Lexical variables are private variables created using my operator.
- Lexical variables are global variables.
- Both of the above.



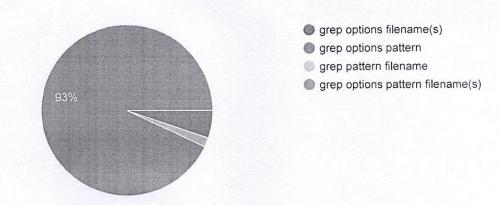
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Which one of the following is the correct syntax for grep command?

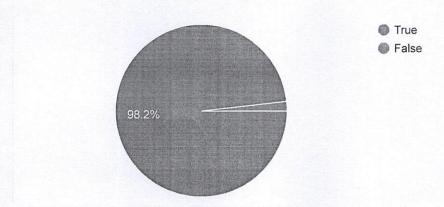
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57 responses



When the pattern is not found in a file, grep command silently returns the prompt.

56 responses



Which of the following commands is known as stream editor?

□ Сору

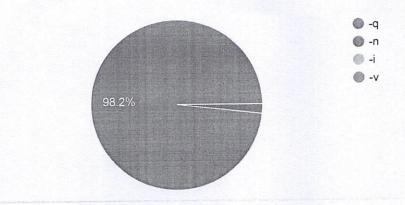
57 responses



To suppress the behavior of 'p' command of sed, we use ____ option.

□ Сору

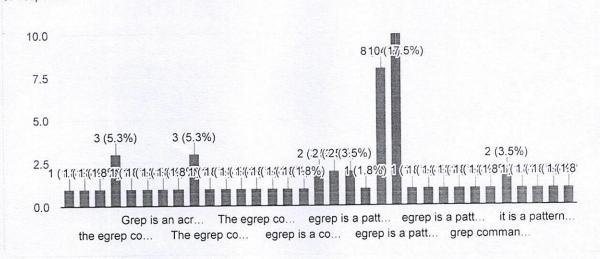
56 responses



Describe egrep command.

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57 responses



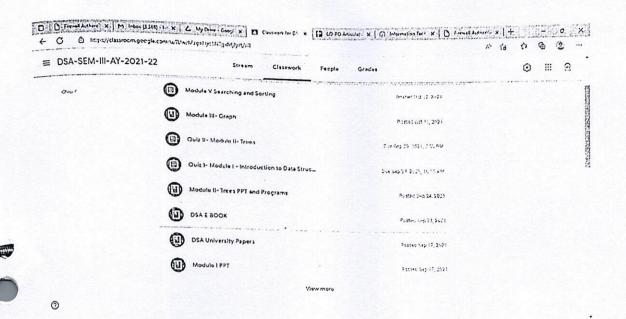
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Shree L. R. Tiwari College of Lingineering Kanakiya Park, Mira Road (E).



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	Description (optional)		•			
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	1. Construct binary search tree 13,3,4,12,14,10,5,1, 8, 2, 7, 9, 11, 6, 18	,				
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Module I- Introduction to Data Structure	ž ; ⊕	
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How do you initialize an array in C?		
() a) int an(3) * (12.3).	•	
() b) int arr(3) = (12.3);	8	
() c) int on[3] = (1,2.3);		
() d) int en(3) = (1,23),		
In a stack, if a user tries to remove an element from an empty stack it is called _	·	
🧳 a) Underflow		
AN Franciscollection		0



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COLLEGE OF ENGINEERING, PUNE

(An Autonomous Institute of Govt. of Maharashtra)
SHIVAJINAGAR, PUNE-411005 (MAHARASHTRA) INDIA

Tel.No.020-25507000/25507009

Fax: 020-25507299

京都在他一点并 年十二十十十年前

E-mail: director@coep.ac.in

No. COEP/VLAB/MP2/NC57/2019-20/001

March 02, 2020

To,

The Principal,
Shree L.R.TIWARI College of Engineering,
Kanakia Park, near Commissioner's Bungalow,
Mira Road (East),
Thane - 401107

Dear Principal,

With reference to your Expression of Interest for Virtual Labs Nodal Centre (VLNC), it gives me immense pleasure to designate your college as a Nodal Centre for Virtual labs. As recommended by you, Prof. Sandeep A. Dwivedi, Electronics & Telecommunication Engineering Department has been nominated as the Nodal Coordinator from your college. This approval is valid up to 31st December 2021 and is subject to the Terms & Conditions attached and any subsequent directives as issued by MHRD.

Kindly acknowledge receipt of this letter and acceptance of the Terms & Conditions.

We thank you again for your interest in the Virtual Labs project and appreciate your endeavor in the service of the student community. Wishing you all the best!

Thank You,

Sincerely,

Prof. S. D. Agashe Principle Investigator,

College of Engineering, Pune

W_=





VIRTUAL LABS: CoE, Pune

(An Initiative of Ministry of Human Resource & Development Under the National Mission on Education through ICT) http://coep.vlab.co.in

Expression of Interest for setting up a Virtual Labs Nodal Centre (VLNC)

ELIGIBILITY:

- 1. The Virtual Labs Nodal Centre (VLNC) should be a Central/State University or Institute/college approved by AICTE/UGC.
- 2. The Institute has to provide a designated/common lab space having 35 PCs or more, with a minimum 1 Mbps internet broadband connection, and a multimedia projector.
- 3. In the event that the internet network operates behind a firewall, the VLNC undertakes to open specific communication ports to facilitate VLab network traffic. Specifically ports 3306, \$900, 5902 and 8700 will need to be opened.
- 4. http://www.java.com/en/download/index.jsp must be downloadable through the internet and Gmail should be accessible.

Terms and Conditions:

- 1. There is no financial liability of any party for using Virtual Labs. It is free to use.
- 2. The VLNC cannot charge students for the use of Virtual Labs.
- 3. One Nodal Coordinator for Virtual Labs should be nominated by the Head of the Institute (Director/Principal) at the respective Nodal Centre.
- 4. Renewal of EOI form is subjected to fair usage of the Virtual Labs.

Nominations and Responsibilities at the Nodal Centres

- 1. Nodal Coordinators will be appointed at each Nodal Centre by the Head of the Institution.
- 2. He/she should continue with his/her responsibilities for at least one semester.
- 3. Requests for change of Nodal Coordinator will not be accepted in the middle of the semester, except in extreme circumstances, for example where the Coordinator proceeds on leave of quits the

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Institute. Nomination of a new Nodal Coordinator will be accepted upon receiving confirmation from the previous Nodal Coordinator.

Responsibilities of the Nodal Coordinator (NC):

- 1. The Nodal Coordinator (NC) will be the main interface between the Outreach Team of Virtual Labs, COE, Pune and the Nodal Centre.
- 2. He/She will conduct training sessions for students and faculty members on in his/her Institute.
- 3. NC may take help from other faculty members and support staff to coordinate training programmes in their respective institutes.
- 4. He/She is required to work towards making Virtual Labs popular among the students.
- 5. He/She is required to help in **Field Testing** the labs and in collation of end-user **Feedback** for the experiments of different Labs.
- 6. He/She is required to help in testing and debugging Virtual Labs while using them.
- 7. A monthly progress report regarding Virtual Labs usage by the students must be submitted by the Nodal Coordinator to the CoE, Pune, duly signed by the Director/Principal of the Institute.
- 8. Each VLNC will organize at least one workshop per semester as an outreach activity of VLabs.
- 9. Faculty members and students from at least three nearby Institutions should attend the workshop. The targeted attendees in each workshop should be approximately 100. The list of participants should be shared with CoE, Pune.
- 10. NCs will coordinate workshops. If required, the Outreach team from CoE, Pune may be invited.
- 11. Reimbursement of expenses maximum upto Rs.6000 will be paid on submission of all workshop bills and receipts, to the Outreach Team, CoE, Pune, within the stipulated time.
- 12. The Outreach Team, CoE, Pune should be informed about workshop dates and other training sessions organized by the Nodal Centre.
- 13. The Nodal Coordinator should submit a report of each workshop conducted, with relevant photographs. A template for the report will be shared by CoE, Pune.
- 14. Honorarium to Nodal Coordinator will be based on per student usages of Virtual Labs in his/her Institute and fulfilling all the above-mentioned responsibilities.

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Responsibility of the Outreach Team

- 1. To provide training and hands on practice on Virtual Labs to Nodal Coordinators and supporting staff.
- 2. Training session will be conducted only at CoE, Pune, as per the advertised date and time.
- 3. Maximum two members with Nodal Coordinator from each Nodal Centres may attend the training
- 4, NO TA/DA WIII DE EIVEN TO NODE; EDWIDINETON AND EUPPONING STAFF FOR ASSENDING STAINING STAINING
- 5. Onsite support may be given upon request by Nodal Centres.
- 6. The Outreach team will keep the Nodal Centres informed about the Labs under development and newly developed Labs in Virtual Labs.
- 7. The Outreach team may visit Nodal Centres on workshop day to ensure smooth conduct of the workshop.

A.

Field Trial Methodology:

- 1. Virtual Labs Team organises Workshop as well as hands on practice session on Virtual Labs for various Science & Engineering Institutions, at CoE, Pune.
- 2. Faculty members participate in the workshop and are encouraged to express their interest in nominating their institutes as Nodal Centres for Virtual Labs
- 3. There is a provision of EOI (Expression of Interest) form to become a Nodal Centre of Virtual Labs.
- The EOI form contains all the details, eligibility criteria, role of Nodal Centre as well as the Nodal coordinator.
- Then the Virtual Labs Team conducts faculty training sessions and workshops for students at the Nodal Centres.
- 6. Department wise demonstrations are given to the students.
- 7. Labs are demonstrated according to the syllabus of the ongoing semester.
- 8. Faculty members of the concerned department should be present during the demonstration.
- 9. After the demo of the Virtual Labs, hands on sessions are conducted.
- 10. On completion of the demo and hands on, the faculty members and the students have to submit the feedback forms to the Virtual Labs team.
- 11. These forms are compiled and analysed. The bugs are duly reported to the developers for debugging.
- 12. At the end of the semester, the compiled report of the Virtual Labs usage must be submitted by the Nodal coordinators.

0/-



Expression of Interest for setting up Virtual Labs' Nodal Centre (VLNC)

Name of the Institute; SHREE L. R. TIWARI COLLEGE OF Acronym of Institute: SLRTCE ENGINEERING

Address: kanakia paek, Neae Commissionee's Bungalow, Mied Road

Thane- 401107

Latitude: 19.294333

Longitude: 72.867882

Affiliated to: University of Murobai.

Approved By (AICTE/UGC/University): AICTE

Approval Number: I - 6969851

AISHE Code: C-46313

Branch of Engineering / Science: Extc, ECS, IT, Computed Engg., Mech. & Civil

Number of Students / Faculties: 239 + 200 + 332 + 347 + 343 + 206 Total: 1667 Saudents

Total number of computers available for Virtual Labs use: 🔫 🗢

Internet bandwidth (in mbps): 100 mbps

Name of the Head of Institute / Principal: De. Deven Shah

Email: peincipal@sletce.10

Mobile: 9029726083

4 85 facultics

Proposed Nodal Centre Coordinator (NCC): ME. Sandcep A. Dustueoli

Email: Sondeep. desiredi 236@gmail.com Mobile: 8412848932

Department: Electeonics frelecommunication Engineereing

It is certified that

- a) The institute is recognized by the AICTE/UGC.
- b) The institute has necessary and adequate infrastructure to host the Virtual Labs.
- c) Strict adherence to standard lab procedures and cyber security laws will be followed.
- d) Virtual Labs may withdraw/stop connectivity without giving any prior notice or reasons.
- e) This EOI for Virtual Labs usage is valid up to 31st December 2023 and requires renewal by the coordinating institute for continued support.

Signature & Stamp

Head of Institute / Principal

Shree L. R. Tiwari College of Engineering

MINA ROAD

Date: 03 01 2023

1. Send the duly signed SCANNED COPY of this EOI to Virtual Labs team.

2. Keep the HARD COPY with you in a Virtual Labs file for the record.

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Department	Lab	Experiment	Total Time (hour:min:sec)
Electronics and Communications	s Virtual Microwave Lab	Design and Characterisation of Strip Line	0:0:56
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Department	Lab	Experiment	Total Time (hour:min:sec)
Electronics and Communications	Basic Electronics (Kharagpur)	VI Characteristics of a Diode	0:1:39
Electronics and Communications	Basic Electronics (Kharagpur)	VI Characteristics of a Diode	0:0:59
Electronics and Communications	Basic Electronics (Kharagpur)	Half Wave Rectification	0:1:45
Electronics and Communications	Digital Electronic Circuits Laboratory (Kharagpur)	Analysis of Functions of BCD-TO-7-segment Decoder / Driver and Operation of 7-segment LED Display	0:0:57
Electronics and Communications	Digital Electronic Circuits Laboratory (Kharagpur)	Analysis of Functions of BCD-TO-7-segment Decoder / Driver and Operation of 7-segment LED Display	0:0:36
Electronics and Communications	Digital Signal processing virtual laboratory (Kharagpur)	Study of sampling theorem, effect of undersampling	0:1:12
Electronics and Communications	Virtual Microwave Lab	Design and Characterisation of Strip Line	0;1:54
Electronics and Communications	Virtual Microwave Lab	Design and Characterisation of Strip Line	0:12:19
Electronics and Communications	Virtual Microwave Lab	Design and Characterisation of Strip Line	0:15:50
Electronics and Communications	Virtual Microwave Lab	Design and Characterisation of Microstrip Line	0:6:55
Electronics and Communications	Virtual Microwave Lab	Design and Characterisation of Strip Line	0:1:0
Electronics and Communications	Virtual Microwave Lab	Patch Antenna	0:3:41

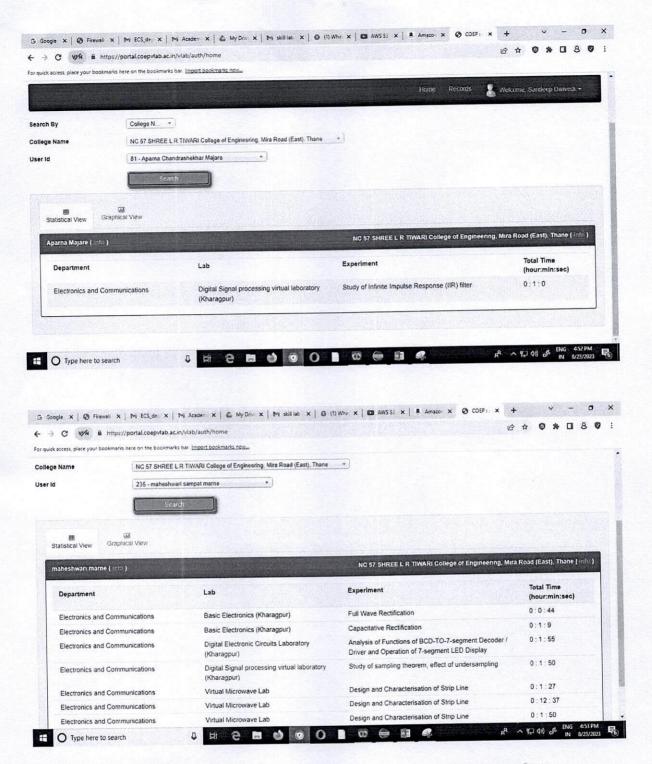
	Lab	Experiment	
Electronics and Communications			(hour:min:sec)
Electronics and Communications	Basic Electronics (Kharagpur)	VI Characteristics of a Diode	0:1:56
	Basic Electronics (Kharagpur)	Full Wave Rectification	0:0:47
Electronics and Communications	Basic Electronics (Kharagpur)	BJT Common Emitter Characteristics	0:1:10
Electronics and Communications	Basic Electronics (Kharagpur)	BJT Common Base Characteristics	0:0:37
Electronics and Communications	Basic Electronics (Kharagpur)	RC Differentiator and Integrator	0:1:15
Electronics and Communications	Digital Electronic Circuits Laboratory (Kharagpur)	Analysis of Functions of BCD-TO-7-segment Decoder / Driver and Operation of 7-segment LED Display	0:1:42
Electronics and Communications	Basic Electronics (Kharagpur)	VI Characteristics of a Diode	0:0:54
Electronics and Communications	Digital Signal processing virtual laboratory (Kharagpur)	Study of sampling theorem, effect of undersampling	0:0:30
Electronics and Communications	Digital Signal processing virtual laboratory (Kharagpur)	Study of Quantization of continuous-amplitude, discrete- time analog signals	0:0:51
Electronics and Communications	Digital Signal processing virtual laboratory (Kharagpur)	Study of Discrete Fourier Transform (DFT) and its inverse	0:1:47
Electronics and Communications	Digital Signal processing virtual laboratory (Kharagpur)	Study of FIR filter design using window method: Lowpass and highpass filter	0:1:35



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Department	Lab	Experiment	Total Time (hour:min:sec)	
Electronics and Communications	Digital Signal processing virtual laboratory (Kharagpur)	Study of convolution: series and parallel system	0:1:18	
Electronics and Communications	Basic Electronics (Kharagpur)	VI Characteristics of a Diode	0:1:40	
Electronics and Communications	Basic Electronics (Kharagpur)	VI Characteristics of a Diode	0:1:26	
Electronics and Communications	Basic Electronics (Kharagpur)	VI Characteristics of a Diode	0:1:25	
Electronics and Communications	Basic Electronics (Kharagpur)	Half Wave Rectification	0:1:50	
Electronics and Communications	Basic Electronics (Kharagpur)	VI Characteristics of a Diode	0:1:33	
Electronics and Communications	Basic Electronics (Kharagpur)	Full Wave Rectification	0:1:18	
Electronics and Communications	Basic Electronics (Kharagpur)	Capacitative Rectification	0:1:60	
Electronics and Communications	Basic Electronics (Kharagpur)	Zener Diode-Voltage Regulator	0:0:54	
Electronics and Communications	Basic Electronics (Kharagpur)	BJT Common Emitter Characteristics	0:0:58	
Electronics and Communications	Basic Electronics (Kharagpur)	BJT Common Base Characteristics	0:1:11	
Electronics and Communications	Basic Electronics (Kharagpur)	RC Differentiator and Integrator	0:0:47	
Electronics and Communications	Digital Electronic Circuits Laboratory (Kharagpur)	Analysis of Functions of BCD-TO-7-segment Decoder / Driver and Operation of 7-segment LED Display	0:0:41	

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Department	Lab	Experiment	Total Time (hour:min:sec)
Electronics and Communications	Basic Electronics (Kharagpur)	VI Characteristics of a Diode	0:1:8
Electronics and Communications	Basic Electronics (Kharagpur)	Half Wave Rectification	0:0:55
Electronics and Communications	Basic Electronics (Kharagpur)	VI Characteristics of a Diode	0:1:55
Electronics and Communications	Basic Electronics (Kharagpur)	BJT Common Emitter Characteristics	0:1:40
Electronics and Communications	Basic Electronics (Kharagpur)	BJT Common Base Characteristics	0:1:27
Electronics and Communications	Basic Electronics (Kharagpur)	BJT Common Base Characteristics	0:1:52
Electronics and Communications	Basic Electronics (Kharagpur)	RC Differentiator and Integrator	0:0:41
Electronics and Communications	Digital Electronic Circuits Laboratory (Kharagpur)	Analysis of Functions of BCD-TO-7-segment Decoder / Driver and Operation of 7-segment LED Display	0:0:46
Electronics and Communications	Digital Electronic Circuits Laboratory (Kharagpur)	Characterization of Digital Logic Families	0:1:59
Electronics and Communications	Digital Electronic Circuits Laboratory (Kharagpur)	Analysis and Synthesis of Boolean Expressions using Basic Logic Gates	0:1:49
Electronics and Communications	Digital Electronic Circuits Laboratory	Analysis and Synthesis of Logic Functions using	0:1:28









Shree Rahul Education Society's (Regd.)

SHREE L. R. TIWARI COLLEGE OF ENGINEERING

Kanakia Park, Near Commissioner's Bungalow, Mira Road (East), Thane 401107, Maharashtra (Approved by AICTE, Govi, of Maharashtra & Affiliated to University of Mumbai)

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Tel. No.: 022-28120144 / 022-28120145 | Email: slrtce@rahuleducation.com | Website: www.slrtce.in

NDLI Resourses Utilization AY -2022-23 ODD

	Cos(Define Cos with Number)	Course name	Video lecture Link
			Z2JzN0ZmU2VhdW5kODBJdWRC
			Tmg3RXNnTDlvdTJmTUJGMXZzW
Subject	CO1:Understand and use basic knowledge in software engineering.	Software Engineering	VJyY2xaTVRpcWNPeFE5S2dDOCt
Code			http://ndl.iitkgp.ac.in/document/
ITC504:So	CO2:dentify requirements, analyze and prepare models.	Software Engineering	Z2JzN0ZmU2VhdW5kODBJdWRC
ftware			http://ndl.iitkgp.ac.in/document/
Engineeri	CO3:Plan, schedule and track the progress of the projects	Software Project management	Z2JzN0ZmU2VhdW5kODBJdWRC
ng			http://ndl.iitkgp.ac.in/document/
	CO4:Design & develop the software solutions for the growth of society	Software Engineering Transcript	Z2JzN0ZmU2VhdW5kODBJdWRC
			http://ndl.iitkgp.ac.in/document/
	CO5:To demonstrate and evaluate real time projects with respect to software	Software Project management	Z2JzN0ZmU2VhdW5kODBJdWRC
			http://ndl.iitkgp.ac.in/document/
	CO6:Apply testing and assure quality in software solution	Software testing	eUJ5RFk4S3pISG1McXpCN2JPTk1

<u>Subject Incharge</u> Mrs. Madhuri N. Gedam HOD Dr.Roopali Lolage



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Shree L. R. Tiwari College of Engineering
Kanakiya Park, Mad Apad (E).



Student Learning Assessment

ALL INDIA COUNCIL FOR TECHNICAL EDUCATION, NEW DELHI





ARUNTHUTHIYAR YOGESH THANGAVELU (SLAS1194869)

3rd Year

INFORMATION TECHNOLOGY

SHREE L.R. TIWARI COLLEGE OF ENGINEERING (1-6969851)

Date: 16-02-2023

Type: Assessment by Institute

Overall Rating



★★★★ Excellent

★★★★ Very Good ★★★★ Good





- You can be a thrill personality to work with and holds a wide range of skills.
- Good technical and leadership skills.

Artificial Intelligence



- Can be Professional and very skilled person.
- You can be a capable and skilled engineer.

Chemistry



- You are a very adaptive and talented engineer.
- Profound professional knowledge and always aims to learn and acquire more skills.

Compiler Design



- Understands complicated issues, even when beyond your direct area of expertise.
- You acquire the peak of understanding the utmost complex design and implementation issues at every s

Data Analytics



- You can be an expert and a highly skilled engineer and leader.
- Your technical and leadership skills are matchable with excellent people

Databases



- You are an outstanding engineer with rich innovations
- You can get into the details with any architect or engineer.



Shree L. R. Tiwari College of Engineering Kanakiya Park, Mira Road (E).

Maths

Machine Learning



- · Professional and result oriented.
- You can be a capable and skilled engineer.
- Losers quit when they're fired. Winners quit when they've won.
- Keep going. Everything you need will come to you at the perfect time.

Natural Language Processing



- Your skills will be remarkable when compared with other engineers.
- You are technically sound, good, fit and innovative.

Operating System



- You can be among the engineers who can do all the tasks effortlessly.
- You acquire the peak of understanding the utmost complex design and implementation issues at every s

Programming And Data Structures



- You could become industrial experts who can explain the complex concepts in a very practical and sim
- You can be an innovative engineer artist who can improvise and confront new situations.

Software Engineering



- You have good understanding of engineering and business pocessess.
- · Highly skilled professional.

Web Technology



- You are a bright, able and an adept engineer.
- A little progress each day adds up to big results

3D Printing and Design



- Profound professional knowledge and always aims to learn and acquire more skills.
- You can be a skilled and highly intelligent engineer.

Artificial Intelligence/ Machine Learning



- Understands complicated issues, even when beyond your direct area of expertise.
- You can be among the engineers who can do all the tasks effortlessly.

Cyber Security



- Your sound, knowledgeable skills and out-of-box thinking can be precious for your circle.
- You can be among the engineers who can do all the tasks effortlessly.

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PRINCIPAL

Shree L. R. Tiwari College (1.17 neering Kanakiya Park, Mira (1.15 (3)).

pata Science



- · You can be an innovative engineer artist who can improvise and confront new situations.
- · Blend of technical and practical skills.

Internet of Things (IoT)



- You possess many skills that an ordinary engineer doesnt think of.
- You can be an eager engineer who is willing to take on any new challenge.

General Knowledge Aptitude



Robotics



- You can be One of the best engineers in our industry.
- Feasible and valuable engineer.

- You acquire the peak of understanding the utmost complex design and implementation issues at every s
- Your sound, knowledgeable skills and out-of-box thinking can be precious for your circle.

Integrity Aptitude



- Focus on your goal. Don't look in any direction but ahead.
- Don't try to be perfect. Just try to be better than you were yesterday.



- You are much hardworking and always complete all of work with integrity.
- Your integral values make others comfortable.

Physical Aptitude



- You possess good managerial skills that help you excel in obtaining management support.
- You use your relevant knowledge, know-how, and skills to explain and clarify thoughts and ideas.

Spiritual Aptitude



- Losers quit when they're tired. Winners quit when they've
- Focus on your goal. Don't look in any direction but ahead.

Social Aptitude

Mental Aptitude



- You have a pleasing personality.
- You are compassionate toward others.



Shree L. R. Tiwari College of Engineering Kanakiya Park, Lufa Road (E).



Student Learning Assessment

ALL INDIA COUNCIL FOR TECHNICAL EDUCATION, NEW DELHI





RISHABH SINGH (SLAS1201856) 3rd Year INFORMATION TECHNOLOGY SHREE L.R. TIWARI COLLEGE OF ENGINEERING (1-6969851)

Date: 27-01-2023

Type: Assessment by Institute

Overall Rating



*** Excellent

★★★★ Very Good ★★★★ Good



- Artificial Intelligence

- You can be an expert and a highly skilled engineer and leader.
- You can be an insightful and thorough researcher and
- Professional and result oriented.
- You can be an application engineer.

Chemistry

 You acquire the peak of understanding the utmost complex design and implementation issues at every s You can be an insightful and thorough researcher and engineer.

Cloud Computing

- ***
- You are an outstanding engineer with rich innovations
- You are knowledgeable in both the creating and engineering.

Compiler Design

- You can demonstrate outstanding, technical and business
- knowledge in the project area. Highly skilled professional.

Computer Networks

- ***
- You can be an exceptional and a great engineer.
- Understands complicated issues, even when beyond your direct area of expertise.



Shree L. R. Timari Co.

ineering

Databases



Machine Learning



- · You can be a skilled and highly intelligent engineer.
- You possess many skills that an ordinary engineer doesnt think of.
- You have good understanding of engineering and business pocessess.
- · Professional and result oriented.

Operating System



- Combined with your dedication and outstanding technical insight give you the availability to delive
- You can be among the engineers who can do all the tasks effortlessly.

Theory Of Computation



- You are virtuous in engineering.
- You can be a thrill personality to work with and holds a wide range of skills.

Web Technology



- You possess many skills that an ordinary engineer doesnt think of.
- You can get into the details with any architect or engineer.

3D Printing and Design



- Combined with your dedication and outstanding technical insight give you the availability to delive
- Profound professional knowledge and always aims to learn and acquire more skills.

Artificial Intelligence/ Machine Learning



You are virtuous in engineering.

You can be a capable and skilled engineer.

Augmented Reality (AR)/ Virtual Reality (VR)



- You can be an insightful and thorough researcher and engineer.
- Understands complicated issues, even when beyond your direct area of expertise.

Cyber Security



- A little progress each day adds up to big results
- It's not about perfect. It's about effort.

Data Science



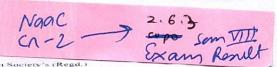
- Don't try to be perfect. Just try to be better than you were yesterday.
- It's not about perfect. It's about effort.



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Kanakiya Park, Mira Road (E)





Shree Rahul Education Society's (Regd.) REE L. R. TIWARI EGE OF ENGINEERING

Pass percentage of Students during last five years
Note (Without Rackless)

Year	Program Code	Program Name	Number of students appeared in the final year examination	Number of students passed in final year examination
	342319110	Civil Engineering	75	72
	342324510	Computer Engineering	84	82
	342337610	Electronics Engineering	62	62
2018-19	342337210/342337280(DSE)	Electronics and Telecommunication	131	124
	342324610	Information Technology	79	78
	342361210/342361280(DSE)	Mechanical Engineering	129	124
	342319110	Civil Engineering	77	77
	342324510	Computer Engineering	71	71
	342337610	Electronics Engineering	37	37
2019-20	342337210/342337280(DSE)	Electronics and Telecommunication	110	110
	342324610	Information Technology	70	70
	342361210/342361280(DSE)	Mechanical Engineering	124	124
	342319110	Civil Engineering	79	79
	342324510	Computer Engineering	75	75
2020-21	342337610	Electronics Engineering	39	39
	342337210/342337280(DSE)	Electronics and Telecommunication	87	87
	342324610	Information Technology	81	81
	342361210/342361280(DSE)	Mechanical Engineering	158	158
	342319110	Civil Engineering	67	60
	342324510	Computer Engineering	76	74
2021-22	342337610	Electronics Engineering	53	50
2021-22	342337210/342337280(DSE)	Electronics and Telecommunication	78	75
	342324610	Information Technology	66	66
	342361210/342361280(DSE)	Mechanical Engineering	79	70
	342319110	Civil Engineering	72	result awaited
	342324510	Computer Engineering	74	result awaited
2022-23	342337610	Electronics Engineering	12	result awaited
2022-23	342337210/342337280(DSE)	Electronics and Telecommunication	66	result awaited

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342324610

342361210/342361280(DSE)

Information Technology

Mechanical Engineering

Shree L. R. Tiwari College of Engineeri National Engineering Mara Rose (S).

result awaited

result awaited

72